

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant : | Satoshi Seo et al. | Art Unit : | 1774 |
| Serial No. : | 10/622,504 | Examiner : | Dawn Garrett |
| Filed : | July 21, 2003 | Conf. No. : | 4688 |
| Title : | MATERIAL FOR AN ELECTROLUMINESCENCE ELEMENT AND ELECTROLUMINESCENCE ELEMENT USING THE SAME | | |

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: 2/8/07

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| Substitute Form PTO-1449 (Modified) | U.S. Department of Commerce Patent and Trademark Office | Attorney's Docket No. 12732-160001 | Application No. 10/622,504 |
| Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b)) | | Applicant Satoshi Seo et al. | |
| | | Filing Date July 21, 2003 | Group Art Unit 1774 |

U.S. Patent Documents

| Examiner Initial | Desig. ID | Document Number | Publication Date | Patentee | Class | Subclass | Filing Date If Appropriate |
|------------------|-----------|-----------------|------------------|----------|-------|----------|----------------------------|
| | AA | | | | | | |
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Foreign Patent Documents or Published Foreign Patent Applications

| Examiner Initial | Desig. ID | Document Number | Publication Date | Country or Patent Office | Class | Subclass | Translation | |
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| | AP | | | | | | | |

Other Documents (include Author, Title, Date, and Place of Publication)

| Examiner Initial | Desig. ID | Document |
|------------------|-----------|--|
| | AQ | D. L. Mathine et al., "Heterogeneously Integrated Organic Light-Emitting Diodes With Complimentary Metal-Oxide-Silicon Circuitry," <i>Applied Physics Letters</i> , Vol. 76, No. 26, June 26, 2000, pp. 3849-3851. |
| | AR | M. Pfeiffer et al., "33.3: Invited Paper: OLEDs with Doped Transport Layers for Highly Efficient Displays," <i>SID 03 DIGEST</i> , May 20, 2003, pp. 1076-1079. |
| | AS | |
| | AT | |

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| Examiner Signature | Date Considered |
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. include copy of this form with next communication to applicant. | |